

100 A CONVENTIONAL METHOD OF CALCULATING AGED DELAY TIME OF A LOGIC LEVEL CIRCUIT

## FIG.1 PRIOR ART

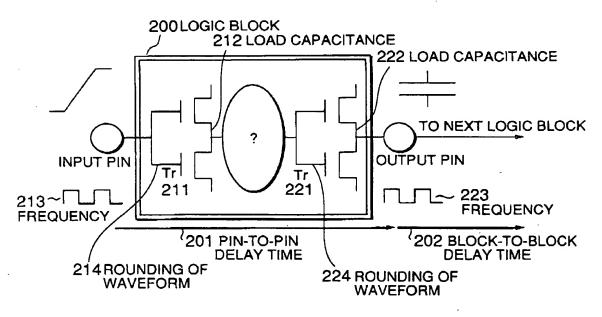


FIG.2

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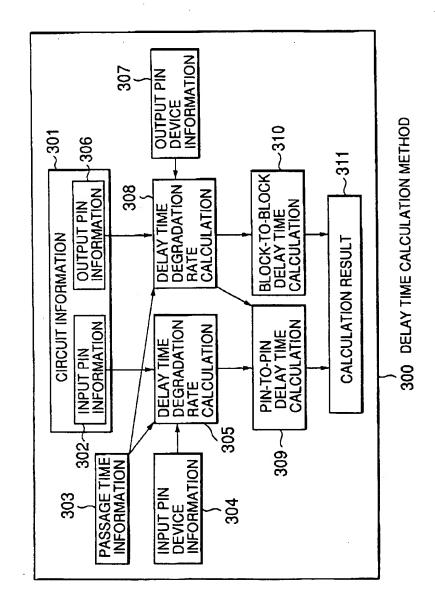
90%

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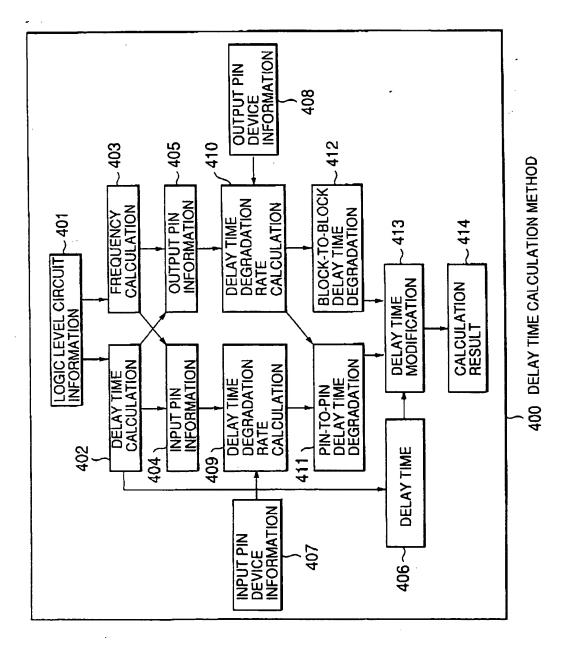




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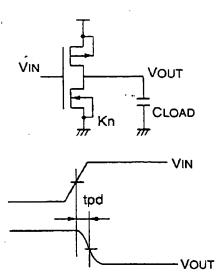
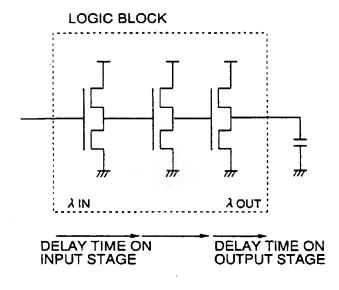


FIG.5



DELAY TIME OF A LOGIC BLOCK

FIG.6







88%